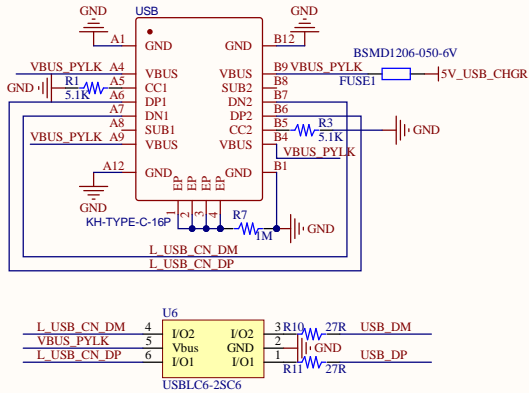
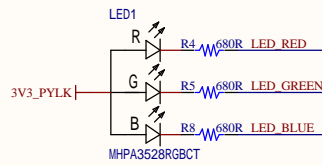


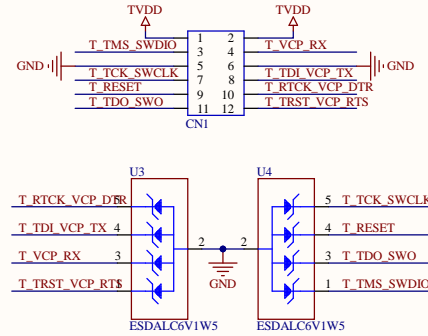
USB



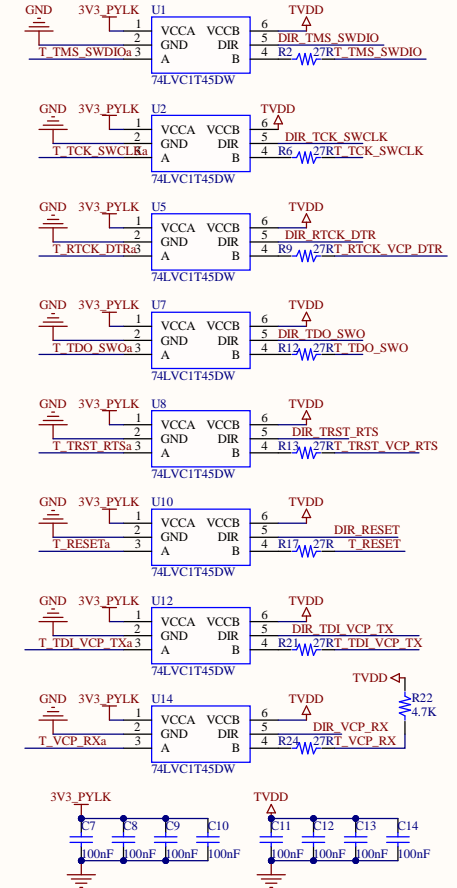
LED



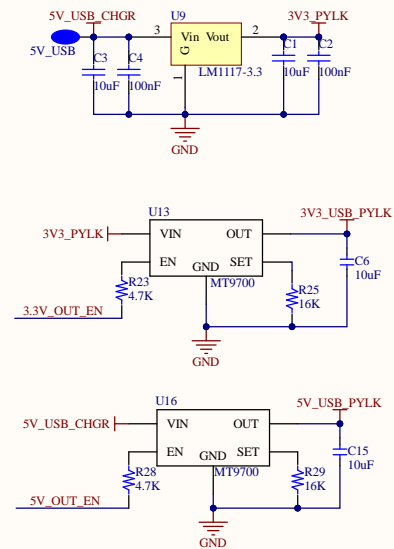
Debug Connector



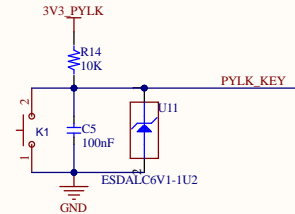
Level Shift



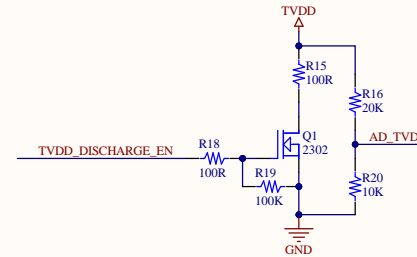
Power



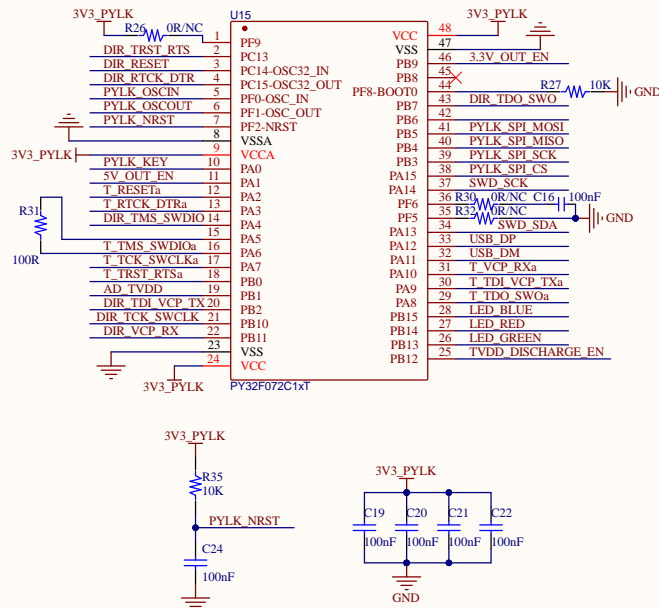
Key



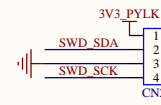
ADC&Electric discharge



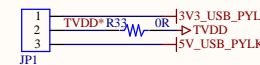
MCU



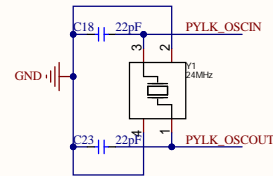
SWD



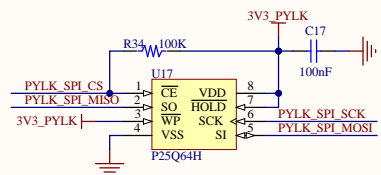
TVDD_Selection



OSC



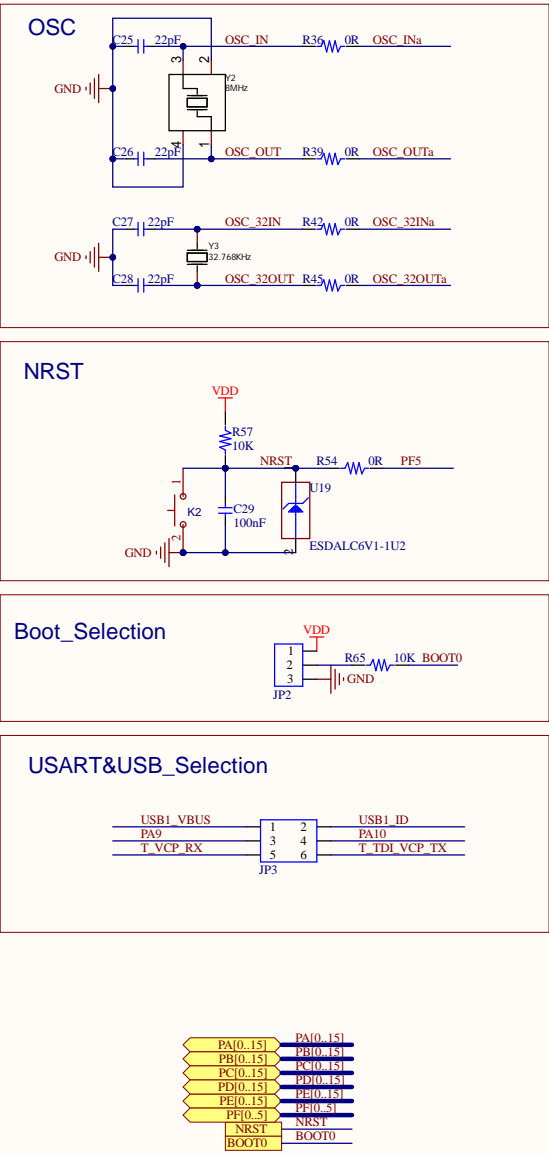
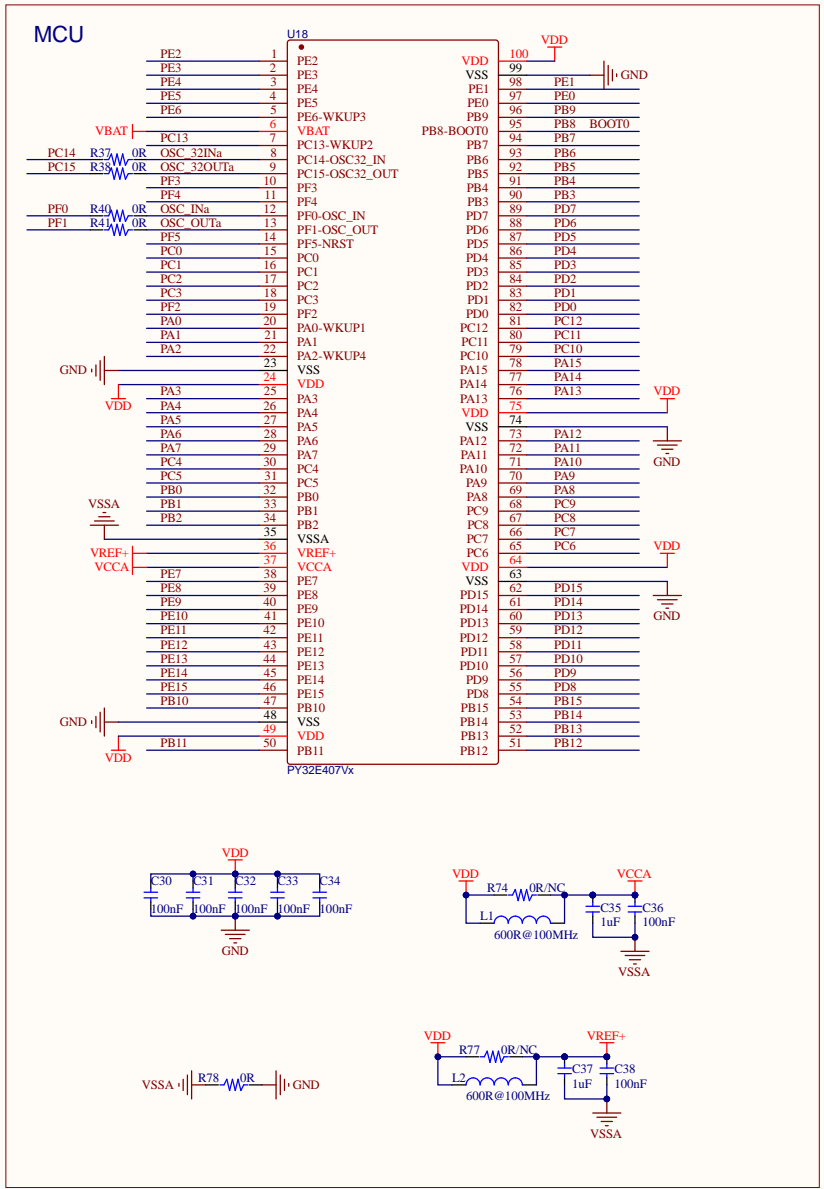
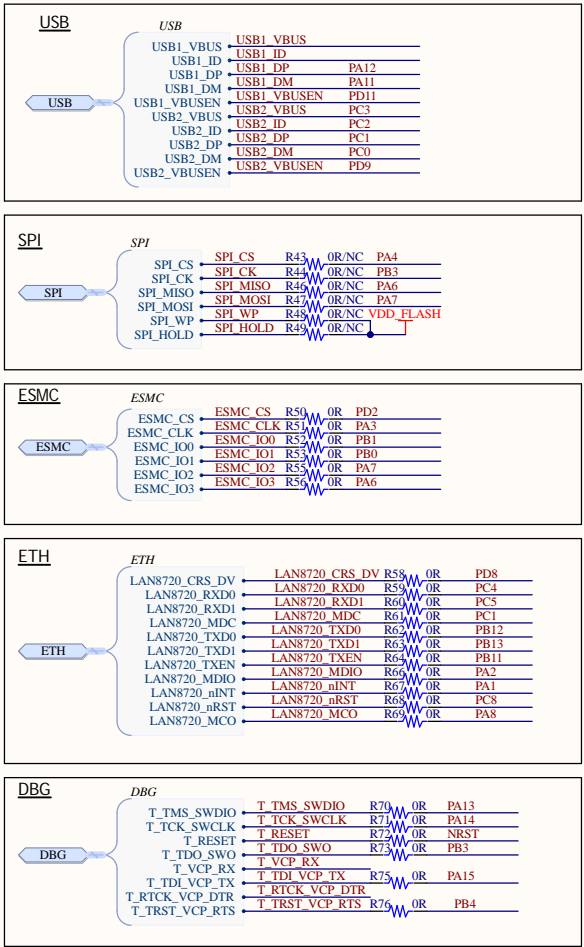
Flash



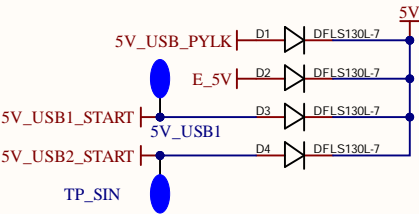
When the DIR pin is set to logic high (1), the data direction is from VCCA to VCCB.
When the DIR pin is set to logic low(0), the data direction is from VCCB to VCCA

DBG	DBG
T_TMS_SWDIO	T_TMS_SWDIO
T_TCK_SWCLK	T_TCK_SWCLK
T_RESET	T_RESET
T_TDO_SWO	T_TDO_SWO
T_VCP_RX	T_VCP_RX
T_TDI_VCP_TX	T_TDI_VCP_TX
T_RTCK_VCP_DTR	T_RTCK_VCP_DTR
T_TRST_VCP_RTS	T_TRST_VCP_RTS

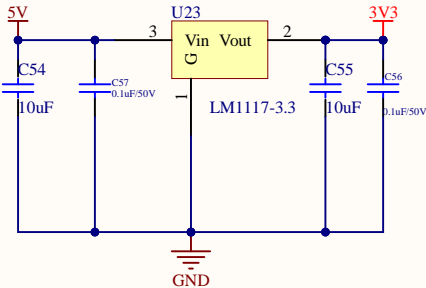




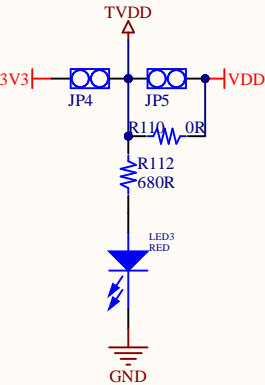
5V_PWR_Selection



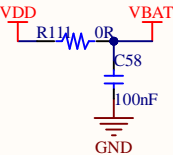
3.3V PWR



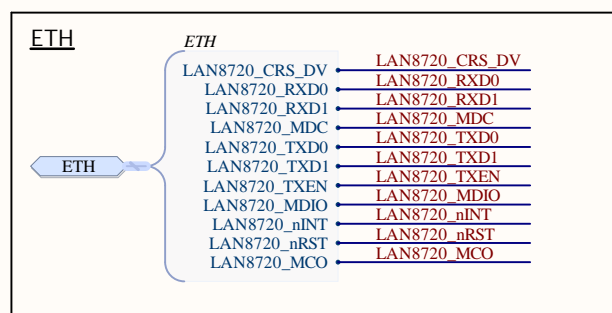
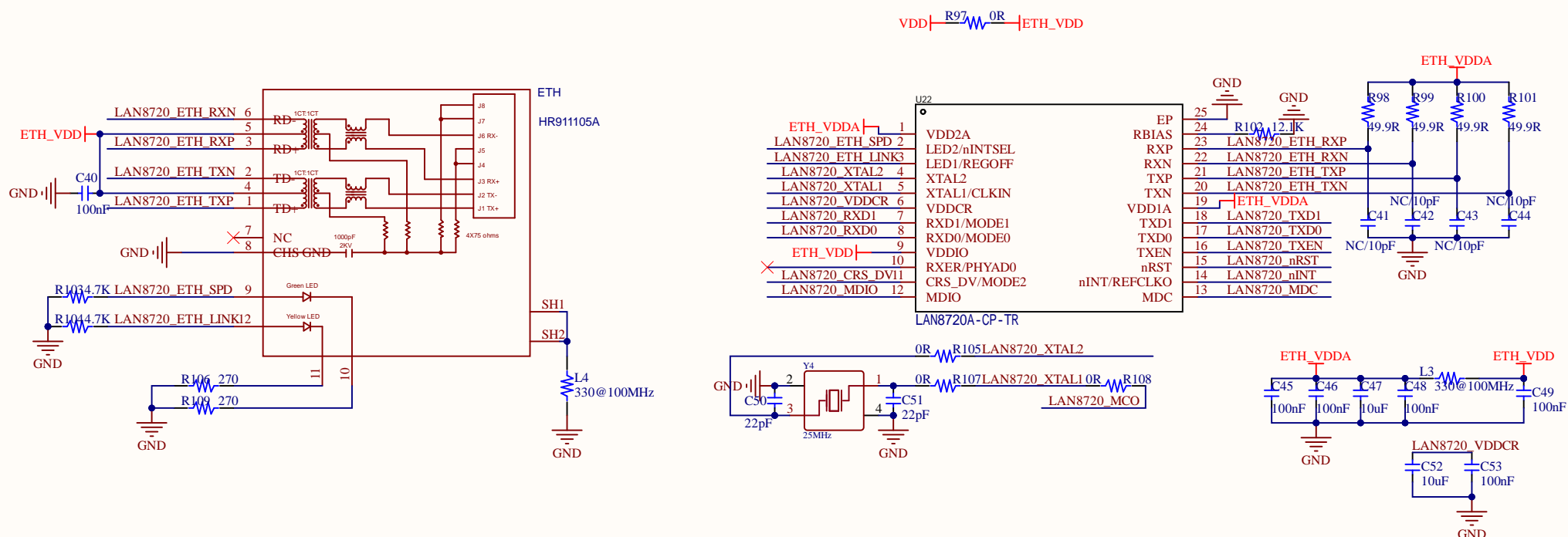
VDD_Selection



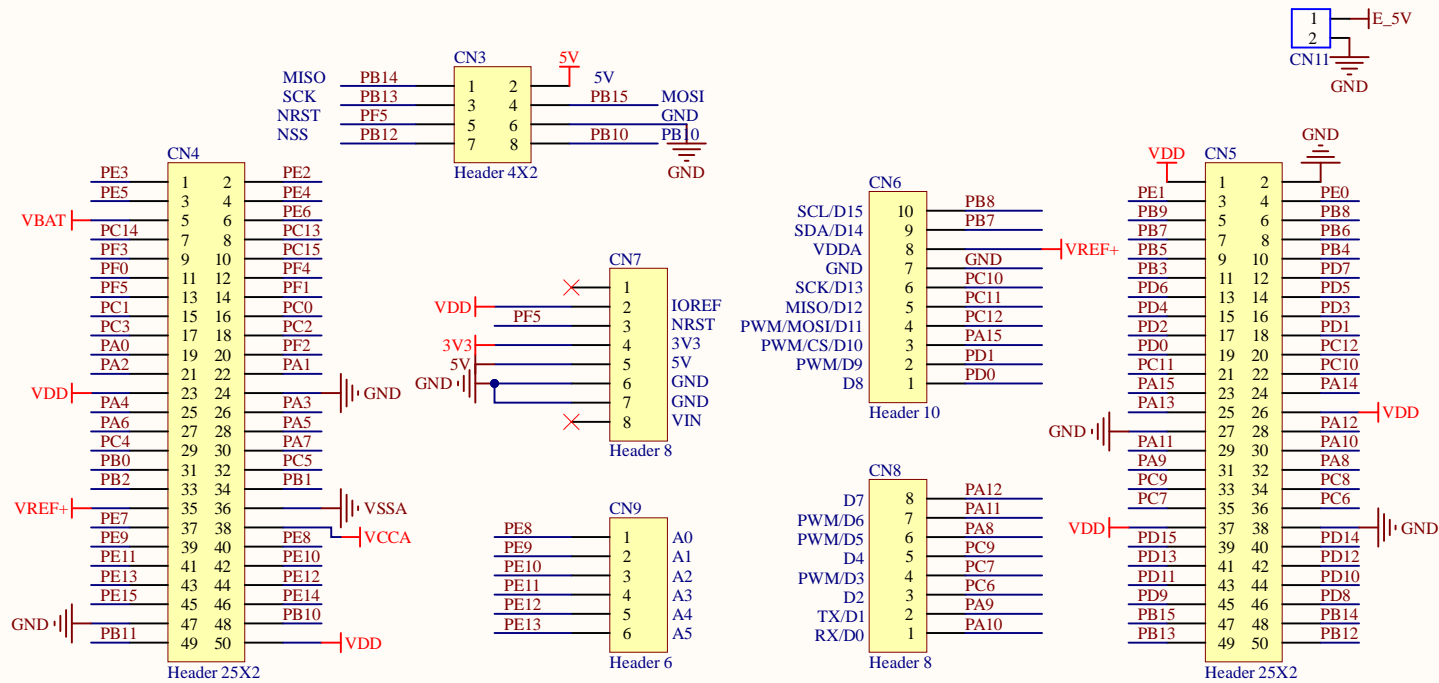
Battery



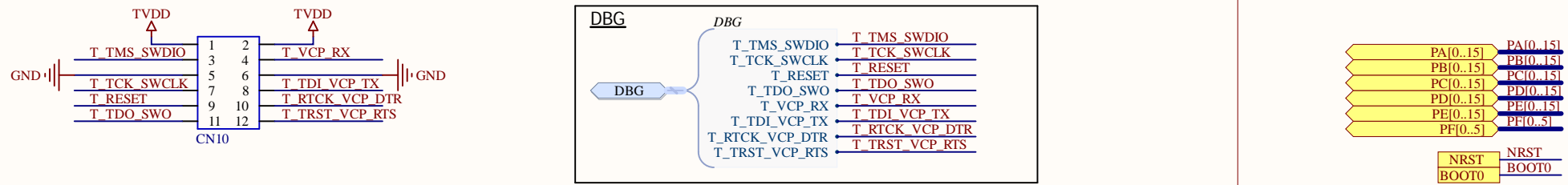
ETH



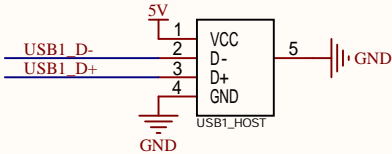
Arduino Connector



Debug Connector



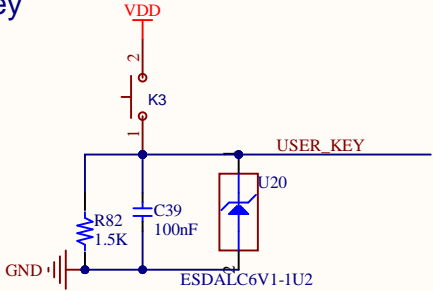
USB1_Host



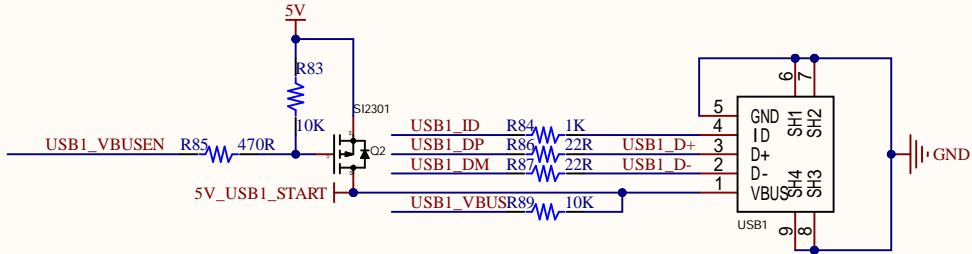
Led



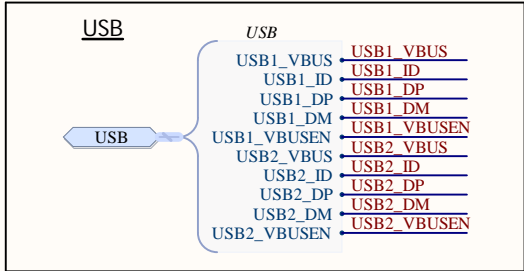
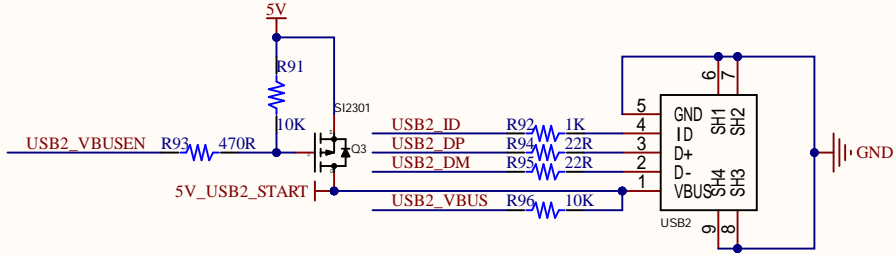
Key



USB1



USB2



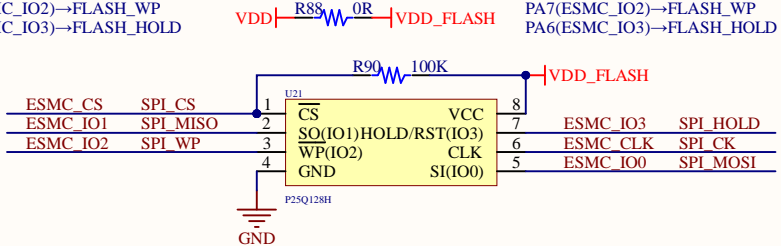
Flash

ESMC Communication

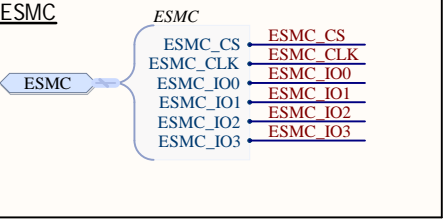
PD2(ESMC_SS1)→CS
PA3(ESMC_CLK)→CLK
PB1(ESMC_IO0)→SI(IO2)
PB0(ESMC_IO1)→SO(IO1)
PA7(ESMC_IO2)→FLASH_WP
PA6(ESMC_IO3)→FLASH_HOLD

SPI Communication

PA4(SPI1_NSS/SPI3_NSS)→CS
PB3(SPI1_SCK/SPI3_SCK)→CLK
PA6(SPI1_MISO/SPI3_MISO)→SO(IO1)
PA7(SPI1_MOSI/SPI3_MOSI)→SI(IO2)
PA7(ESMC_IO2)→FLASH_WP
PA6(ESMC_IO3)→FLASH_HOLD



ESMC



SPI

